

Top 127 results retrieved for the query **Patt multi level branch predict** ([Details](#))

1. Branch Prediction Techniques [new window] [frame] [preview]

... 20 **Two- level Branch Predictors** [Pan, So ... 92, Yeh & **Patt** ISCA'93 ... Gshare **Two- level Predictor Branch History Branch ... predictors Multi -bank BTB with bimodal predictor ... first taken...**

URL: research.ac.upc.es/HPCseminar/SEM9900/alex.ppt - show in clusters

Sources: Lycos 2, Netscape 5

2. SeaWiFS Publications - abstracts [new window] [frame] [preview]

... springtime feature, **multi-platform surveys** (23 ... s facility to **predict** the instrument ... SeaWiFS bands-at the 3% **level**-from the completion of ... Inst Oceanol, So **Branch**, Gelendzhik, Russia ...

URL: seawifs.gsfc.nasa.gov/.../ANNOUNCEMENTS/pub_abstracts.html - show in clusters

Sources: Looksmart 2, MSN 56

3. Citations: Two-level adaptive branch prediction - Yeh, Patt (ResearchIndex) [new window] [frame] [preview]

... 51--61, December 1991. **Multi-stage Cascaded ... Self-citation (Patt) (Correct)Two ... predictors [14]. The 2 level predictors** attain high ... of a conditional **branch**. To **predict** indirect jumps ...

URL: citeseer.com/context/363793/0 - show in clusters

Sources: Looksmart 1

4. Two- Level Adaptive Training Branch Prediction [new window] [frame] [preview]

We focused on dynamic hardware-based **prediction** schemes in which the hardware rearranges the instruction execution to reduce the stalls rather than the compile-time schemes, which require static ... The **first- level branch** execution history is the history of ... on **Two- level Branch Prediction** by Yeh and **Patt** [1-2] ... no difficulty with attempting to **predict** non-existent **branches**. ...

URL: www-users.cs.umn.edu/~kazar/report.doc - show in clusters

Sources: MSN 1

5. Citations: Alternative implementations of two- level adaptive branch ... [new window] [frame] [preview]

... and Yale N. **Patt** . Alternative ... implementations of two- **level adaptive branch prediction** . In Proc ... and Yale N. **Patt** . Alternative ... implementations of two- **level adaptive branch prediction**

URL: citeseer.nj.nec.com/context/109027/71988 - show in clusters

Sources: Lycos 1

6. Citations: Two- level adpative branch prediction and instruction ... [new window] [frame] [preview]

... 21] Technology has changed since their study, and as we show in this paper, a **multi level branch prediction** design is advantageous. Yeh and **Patt** proposed using ...

URL: citeseer.nj.nec.com/context/418779/0 - show in clusters

Sources: Netscape 1

7. A New Direction for Computer Architecture Research [new window] [frame] [preview]

... cores, that uses **multi-level** caching and ... core, small **first level** caches backed by a ... it is difficult to **predict** any potential success ... of out-of-order, **branch prediction** and/or ...

URL: iram.cs.berkeley.edu/papers/direction/paper.html - show in clusters

Sources: Looksmart 6, MSN 24

8. UNIVERSITY COLLEGE LONDON : Department of Physics and Astronomy [new window] [frame] [preview]

... of potential targets, to **predict** what Darwin would be expected ... increasing evidence of some **level** of solar control of the long ... AGB) ascent of the red giant **branch** and the white dwarf end-point ...

URL: www.star.ucl.ac.uk/annual_review.html - show in clusters

Sources: Looksmart 3, MSN 82

9. Citations: Two- level adaptive branchprediction and instruction fetch mechanisms for high performance superscalar process [new window] [frame] [preview]

T. Yeh. Two- **level adaptive branchprediction** and instruction fetch mechanisms for high performance superscalar processors. PhD thesis, Department of Electrical Engineering and Computer Science, ... T-Y Yeh, " **Two- level Adaptive Branch Prediction** and ... extending **2 level branch predictor** [18] so as to **predict** multiple **branches** ... and Yale **Patt** introduced two **level** adaptive **prediction** ...

URL: citeseer.nj.nec.com/context/39718/0 - show in clusters
Sources: MSN 2

10. Citations: Two-level adaptive branch prediction - Yeh, Patt... [new window] [frame] [preview]

... Tse-Yu Yeh and Yale N. Patt . Two-level adaptive branch prediction Multi-stage Cascaded Prediction - Driesen, Hölzle (1999) (1 citation) (Correct). ...

URL: citeseer.nj.nec.com/context/363793/0 - show in clusters
Sources: Netscape 2

11. http://www.cecs.uci.edu/Conference%20Proceedings/iccd_sudeep.pdf [new window] [frame] [preview]

Improving **Branch Prediction** Accuracy in Embedded Processors in the Presence of Context Switches Sudeep Pasricha, Alex Veidenbaum Center for Embedded Computer Systems University of California, ... a static **branch** instruction to **predict** its outcome ... and **Patt** [6] examined the effect of context. switches on two-level **branch prediction** ... In an actual **multi** - programming environment, ...

URL: [www.cecs.uci.edu/Conference Proceedings/iccd_sudeep.pdf](http://www.cecs.uci.edu/Conference%20Proceedings/iccd_sudeep.pdf) - show in clusters
Sources: MSN 13, Netscape 15

12. Citations: Branch target buffer design and optimization - Perleberg, Smith (ResearchIndex)

[new window] [frame] [preview]

Perleberg, C. and Smith, A. J. **Branch** target buffer design and optimization. IEEE Transactions on Computers, 42(4):396--412, April 1993. ... this paper, a **multi level branch prediction** design is advantageous. Yeh and **Patt** proposed using a Basic ... seen target to **predict** the next target for a **branch** . The indexing function ...

URL: citeseer.nj.nec.com/context/109020/0 - show in clusters
Sources: MSN 3

13. A STUDY OF BRANCH PREDICTION TECHNIQUES [new window] [frame] [preview]

... Taxonomy of Two-Level Schemes Background **Branch Prediction** Strategies ... Use Two-Level **branch predictors** with k-bit shift ... index into a 2-level **branch** history table ... **Branch prediction** ...

URL: students.csci.unt.edu/~ss0125/Report.doc - show in clusters
Sources: Lycos 3

14. 25. ISCA 1998 [new window] [frame] [preview]

... S. Chappell, Yale N. **Patt** : An Analysis ... Meir Feder, Sholomo Weiss: **Branch Prediction** Based on ... Cox, Narendra Bhandri, Michael Shantz: **Multi - Level** Texture Caching ...

URL: www.informatik.uni-trier.de/~ley/db/conf/isca/isca98.html - show in clusters
Sources: Netscape 3

15. archive [new window] [frame] [preview]

... The department of defense high level architecture. In Fall ... strategies for time warp on **multi**- user workstations. The Computer ... An approximate method to **predict** performance of a distributed ...

URL: www.cs.bham.ac.uk/research/pdesmas/LITERATURE/archive.html - show in clusters
Sources: Looksmart 5, MSN 78

16. paper.dvi [new window] [frame] [preview]

... correlation between **branches** and its use ... of **branch prediction** was described by Yeh and **Patt** [2], [3 ... Marr and **Patt** reported the use of the 2-level adaptive **branch predictor** for...

URL: www.tinker.ncsu.edu/symposia/pact97.pdf - show in clusters
Sources: Lycos 13, MSN 17

17. hpca, Eighth International Symposium on High-Performance Computer Architectur... [new window] [frame] [preview]

... Scheduling on **Multi-channel** Memory ... Analysis to **Predict** the Outcome of ... Issues Related to **Branch Prediction** ... p. 0275 User-Level Communication in ... D. Brown, Yale N. **Patt** p. 0299 ...

URL: www.computer.org/proceedings/hpca/1525/1525toc.htm - show in clusters
Sources: Looksmart 9, MSN 29

18. 1995 [new window] [frame] [preview]

... scheduling algorithms must incorporate system-level information (e.g., request priorities ... such as ALUs and multipliers connected by **multi-bit** buses. Many modern ICs are composed of ...

URL: www.eecs.umich.edu/eecs/research/techreports/cse95.html - show in clusters
Sources: Looksmart 4

19. **Branch Path Re-Aliasing** [new window] [frame] [preview]

Branch Path Re-Aliasing Daniel A. Jim'enez Department of Computer Sciences The University of Texas at Austin Austin, TX 78712 Deeper pipelines improve overall performance by allow- ing more increased **branch** misprediction ... **branch** outcome should be inverted. before it is recorded in the global history register. Even. in CPUs with **multi** -cycle ...

URL: camino.rutgers.edu/fddo4.pdf - show in clusters
Sources: MSN 4

20. **[www.cecs.uci.edu ...Papers/IJHSC99](http://www.cecs.uci.edu/~alexv/Papers/IJHSC99)** [new window] [frame] [preview]

... In this study, **multi level branch prediction** is used to overcome ... several **branches** . `` **Multi Level "** **Branch Prediction (MLBP)** [YeMP93 ... help to combine **branch prediction** and...

URL: www.cecs.uci.edu/~alexv/Papers/IJHSC99.ps - show in clusters
Sources: Lycos 4

Result Pages: 1-20 - 21-40 - 41-60 - 61-80 - 81-100 - 101-120 - 121-127

Details

Looksmart - Top 10 results retrieved, 95 requested. (5 pages requested - 5 OK)

Lycos - Top 20 results retrieved, 20 requested. (2 pages requested - 2 OK)

MSN - Top 94 results retrieved, 95 requested. (1 page requested - 1 OK)

Netscape - Top 20 results retrieved, 20 requested. (2 pages requested - 2 OK)

Overture - No result retrieved, 30 requested. (1 page requested - 1 OK)

Top 12 results retrieved for the query **prefetching using markov principles Joseph Grunwald 24th annual international symposium** ([Details](#))

1. Cooperative prefetching [\[new window\]](#) [\[frame\]](#) [\[preview\]](#)

... Ravi Sethi , Jeffrey D. Ullman, Compilers: **principles** , techniques, and ... 3 Doug Joseph , Dirk Grunwald , **Prefetching using Markov** predictors, Proceedings of ...

URL: portal.acm.org/...&coll=portal&CFID=11111111&CFTOKEN=2222222 - show in clusters

Sources: Netscape 1

2. Morgan Kaufmann Publishers Web Site Index [\[new window\]](#) [\[frame\]](#) [\[preview\]](#)

Version 2.0 Released August 8, 2000 Table of Contents Welcome to the web component that complements our book Readings in Computer Architecture [1]!

URL: www.bhusa.com/companions/1558605398/appendices - show in clusters

Sources: MSN 1, MSN 2

3. These bibtex bibliographic entries for the 24th % INTERNATIONAL ... [\[new window\]](#) [\[frame\]](#) [\[preview\]](#)

... Formatting), unifies and extends **principles** underlying several ... **Grunwald and Douglas Joseph** ", TITLE = " **Prefetching Using Markov Predictors**", BOOKTITLE ...

URL: www.cs.wisc.edu/arch/www/ISCAbib/isca24.bib - show in clusters

Sources: Netscape 2

4. Morgan Kaufmann Publishers Web Site Index [\[new window\]](#) [\[frame\]](#) [\[preview\]](#)

... Doug Joseph and Dirk Grunwald **Prefetching Using Markov Predictors**. ... in Multiprocessors Through Compiler-Inserted **Prefetching** ... SIMD **principles** are being employed ...

URL: www.cs.wisc.edu/~markhill/readings/www/version_00_08_08.html - show in clusters

Sources: Netscape 3

5. Evaluating the Impact of Memory System Performance on Software ... [\[new window\]](#) [\[frame\]](#) [\[preview\]](#)

... Using the SimpleScalar simulator, we evaluate the impact of memory bandwidth and latency on the effectiveness of software **prefetching** and locality ...

URL: maggini.eng.umd.edu/pub/SoftwareLocality.pdf - show in clusters

Sources: Netscape 4, Netscape 10

6. Recurrence analysis for effective array prefetching in Java [\[new window\]](#) [\[frame\]](#) [\[preview\]](#)

... We evaluate **prefetching using** benchmark programs from the Jama library [16] and the Java Grande benchmark suite [7]. We run the programs on RSIM, a simulator ...

URL: www.cs.utexas.edu/users/mckinley/papers/CCPE-2004.pdf - show in clusters

Sources: Netscape 5

7. Dynamic Speculative Precomputation [\[new window\]](#) [\[frame\]](#) [\[preview\]](#)

... Proceedings of the 34th International Symposium on Microarchitecture ... this work are constructed using back-end ... Dependence Based **Prefetching** [15] targets pointer ...

URL: www.cs.ucsd.edu/users/tullsen/dsp.pdf - show in clusters

Sources: Netscape 6

8. Adaptive Prefetching for Visual Data Exploration [\[new window\]](#) [\[frame\]](#) [\[preview\]](#)

... Chapter 5 discusses our approach to adaptive **prefetching** a subregion of the data display using a mouse ... The **principles** of brushing were first explored by Becker ...

URL: davis.wpi.edu/~xmdv/docs/doshi_msthesis.pdf - show in clusters

Sources: Netscape 7

9. Dynamic Feature Selection for Hardware Prediction [\[new window\]](#) [\[frame\]](#) [\[preview\]](#)

... to time and space constraints, a basic understanding of the **principles** employed by ... algorithms recursively grow a tree from the top down using greedy heuristics ...

URL: www.ece.purdue.edu/~givan/papers/feature-selection.pdf - show in clusters

Sources: Netscape 8, Netscape 9

10. Project Summary [\[new window\]](#) [\[frame\]](#) [\[preview\]](#)

... This idea is similar to hybrid **principles** [46] and hybrid ... of the tangential stiffness matrix **using** a special ... by the team of Cai, **Grunwald** , Heimbigner, McBryan ...

URL: caswww.colorado.edu/CF.d/NSF97.d/proposal.pdf - show in clusters

Sources: Netscape 11

11. Evaluating the Impact of Memory System Performance on Software ... [new window] [frame] [preview]

... **Prefetching using Markov** In Proceedings of the Sixth ACM SIGPLAN **Symposium on Principles and Practice of Parallel ...** of a Compiler Algorithm for **Prefetching**

URL: www.cs.umd.edu/projects/cosmic/papers/ics01-pref.ps - show in clusters

Sources: Netscape 12

12. Masking Memory Access Latency with a Compiler-Assisted Data ... [new window] [frame] [preview]

... well-conformed looping structures, the **use** of explicit fetch instructions exacts a performance penalty that must be considered when **using** software **prefetching**

URL: www.arctic.umn.edu/papers/svw-phd-thesis-98.pdf - show in clusters

Sources: Netscape 13

Details

Looksmart - No result retrieved, 95 requested. (5 pages requested - 5 OK)

Lycos - No result retrieved, 20 requested. (2 pages requested - 1 OK - 1 partial)

MSN - Top 2 results retrieved, 95 requested. (1 page requested - 1 OK)

Netscape - Top 13 results retrieved, 20 requested. (2 pages requested - 2 OK)

Overture - No result retrieved, 30 requested. (1 page requested - 1 OK)

Top 46 results retrieved for the query **Chrysos Emer Memory Dependence prediction using store sets**
([Details](#))

1. **[VSSAD](#)** [new window] [frame] [preview]

... **Memory Dependence Prediction using Store Sets**, George Chrysos and Joel Emer . Published at ISCA25.

...

URL: emer.org/Family/Joel/Professional - show in clusters

Sources: MSN 7, Netscape 14

2. **[Method and apparatus for predicting memory dependence using store sets \(US6108770\)](#)** [new window] [frame] [preview]

My Account | Products Search: Quick/Number Boolean Advanced Derwent Help Company History | Partners | Privacy Policy | News | Events | Web Seminars | Contact Us The Delphion Integrated View Buy ...

URL: www.delphion.com/details? - show in clusters

Sources: MSN 1

3. **[Method and apparatus for predicting memory dependence using store ...](#)** [new window] [frame] [preview]

... **predicting memory dependence using store sets** [Derwent ... Inventor: **Chrysos** , George Z.; Marlboro, MA **Emer** , Joel S ... separate **store sets** are merged ... operation **Memory** Communication via...

URL: www.delphion.com/details?&pn=US06108770 - show in clusters

Sources: Lycos 1

4. **[Citations: Memory dependence prediction using store sets - Chrysos ...](#)** [new window] [frame] [preview]

G. Chrysos and J. Emer . **Memory dependence prediction using store sets** . In 25th Annual International Symposium on Computer Architecture, June 1998. ...

URL: citeseer.nj.nec.com/context/270106/0 - show in clusters

Sources: Netscape 1

5. **[Architecture prelim study list](#)** [new window] [frame] [preview]

... George Z. **Chrysos** and Joel S. **Emer** , " **Memory Dependence Prediction using Store Sets**",. *Mikko H. Lipasti, Christopher B. ...

URL: www.cs.berkeley.edu/~yatish/prelim/prelim.html - show in clusters

Sources: MSN 10, Netscape 12

6. **[Coherence Communication Prediction in Shared- Memory Multiprocessors - Kaxiras, Young \(ResearchIndex\)](#)** [new window] [frame] [preview]

... et al. - 1988. 40 **Memory Dependence Prediction using Store Sets** (context) - **Chrysos** , **Emer** - 1998. 10 Multicast Snooping: ...

URL: citeseer.nj.nec.com/kaxiras00coherence.html - show in clusters

Sources: MSN 2

7. **[DBLP: George Z. Chrysos](#)** [new window] [frame] [preview]

... Google - HomePageSearch 1998 2 EE George Z. **Chrysos** , Joel S. **Emer** : **Memory Dependence Prediction Using Store Sets** . ISCA 1998: 142-153 1997 1 EE Jeffrey Dean ... Waldspurger, William E. Weihl ...

URL: dblp.uni-trier.de/db/indices/a-tree/c/Chrysos@George_Z=.html - show in clusters

Sources: Lycos 2

8. **[Citations: Memory Dependence Prediction - Moshovos \(ResearchIndex\)](#)** [new window] [frame] [preview]

... **Chrysos** and **Emer** [2] introduced the **store set** concept which allowed ... **Memory Dependence Prediction** . PhD thesis, University of Wisconsin - Madison, 1998. ...

URL: citeseer.nj.nec.com/context/1109531/0 - show in clusters

Sources: Netscape 2

9. **[Background Reading for the Architecture Preliminary Exam](#)** [new window] [frame] [preview]

... George Z. **Chrysos** and Joel S. **Emer** , " **Memory Dependence Prediction using Store Sets**", Proceedings of the International ...

URL: www.cs.berkeley.edu/~jaein/ar.html - show in clusters

Sources: MSN 11, Lycos 17

10. Memory Bypassing: Not Worth the Effort [new window] [frame] [preview]

Memory Bypassing: Not Worth the Effort Gabriel H. Loh Daniel H. Friendly Dept. of Computer Science Dept. of Computer Science Dept. of Electrical Engineering **Memory dependence prediction ... Abstract. Memory dependence prediction** establishes a read after. write **dependence** between a store ... lar, **Chrysos** and **Emer** proposed using **Store Sets** to pre-. dict **memory dependences** [3]. ...

URL: www.cs.yale.edu/~loh/Papers/wddd2002-bp.pdf - show in clusters

Sources: MSN_3

11. DBLP: Joel S. Emer [new window] [frame] [preview]

Joel S. Emer List of publications ... **Dynamic Branch Prediction** to Reduce Destructive ... **Calder, Joel S. Emer :** Reducing cache misses using hardware and ... **EE George Z. Chrysos , Joel S. Emer : Memory ...**

URL: www.vldb.org/dblp/db/indices/a-tree/e/Emer:Joel_S=.html - show in clusters

Sources: Lycos_3, Lycos_4

12. Memory dependence prediction using store sets [new window] [frame] [preview]

... **Memory dependence prediction using store sets** . Full text, Full text available on the Publisher sitePublisher Site pdf formatPdf (1.66 MB). ...

URL: portal.acm.org/...M&coll=GUIDE&CFID=11111111&CFTOKEN=2222222 - show in clusters

Sources: Netscape_3

13. Reading List (EE482 - Spring 1999/2000) [new window] [frame] [preview]

... No. 5, May 1996. G. **Chrysos** and J. **Emer** , " **Memory Dependence Prediction Using Store Sets**", in **Proceedings of the 25h ...**

URL: cva.stanford.edu/ee482a/readlist_v1.htm - show in clusters

Sources: MSN_12, Netscape_17

14. Memory Dependence Prediction using Store Sets [new window] [frame] [preview]

Memory Dependence Prediction using Store Sets George Z. **Chrysos** and Joel S. **Emer** Digital Equipment Corporation Hudson, MA 01749 For maximum performance, an out-of-order processor must issue load ...

URL: www.math.tau.ac.il/~ohad/PapresClass/P42.pdf - show in clusters

Sources: MSN_4

15. Memory Dependence Prediction using Store Sets [new window] [frame] [preview]

Memory Dependence Prediction using Store Sets George Z. **Chrysos** and Joel S. **Emer** Digital Equipment Corporation Hudson, MA 01749 { **chrysos** , **emer** }@vssad.hlo.dec ...

URL: www.cs.utah.edu/classes/cs7960-rajeev/papers/chrysos98.pdf - show in clusters

Sources: Netscape_4

16. Extra speculative execution papers [new window] [frame] [preview]

... **Synchronization of Data Dependences** . In **Proceedings ... Performance of Memory Communication ...** George **Chrysos** and Joel **Emer** . **Memory Dependence Prediction using Store Sets** . To appear

URL: www.cc.gatech.edu/~kenmac/8113/spec-extra.html - show in clusters

Sources: Lycos_16, Netscape_20

17. A High-Bandwidth Memory Pipeline for Wide Issue Processors - Cho, Yew, Lee (ResearchIndex)

[new window] [frame] [preview]

... **Sohi** - 1990. 40 **Memory Dependence Prediction Using Store Sets** (context) - **Chrysos** , **Emer** - 1998. 39 **Streamlining Inter ...**

URL: citeseer.nj.nec.com/cho01highbandwidth.html - show in clusters

Sources: MSN_5

18. Dynamic Memory Disambiguation in the Presence of Out-of-order Store... [new window] [frame] [preview]

... level parallelism by using a **memory dependence predictor** to guide ... al. - 1999 40 **Memory dependence prediction using store sets** (context) - **Chrysos** , **Emer** - 1998 21 **Predictive ... Gupta** -...

URL: citeseer.nj.nec.com/289258.html - show in clusters

Sources: Lycos_5

19. Cost Effective Memory Dependence Prediction Using Speculation ... [new window] [frame] [preview]

... Emphasis has been given to identifying the precise **store** instruction a load may **depend** on. **Store - set Memory Dependence Predictor (Chrysos & Emer - 1998)**. ...

URL: mooss.csc.ncsu.edu/pact02/slides/onder265.ppt - show in clusters

Sources: Netscape 5, Netscape 6

20. Hardware Support for Compiler Memory Optimizations: [new window] [frame] [preview]

... **Memory Dependence Prediction using Store Sets. Z. Chrysos and J. Emer (ISCA '98)** ...

URL: www.cs.wisc.edu/~bodik/teaching/Slides/mcb.pdf - show in clusters

Sources: MSN 6

Result Pages: 1-20 - 21-40 - 41-46

Details

Looksmart - No result retrieved, 95 requested. (5 pages requested - 5 OK)

Lycos - Top 20 results retrieved, 20 requested. (2 pages requested - 2 OK)

MSN - Top 17 results retrieved, 95 requested. (1 page requested - 1 OK)

Netscape - Top 20 results retrieved, 20 requested. (2 pages requested - 2 OK)

Overture - No result retrieved, 30 requested. (1 page requested - 1 OK)



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